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75	E ADDRESS (Note: Use Block 1 for 590 07/26/2004 ANCOS, P.L.L.C.	any change of address)	papers. Each addition have its own certificat	f mailing can only be used finis certificate cannot be used all paper, such as an assignme te of mailing or transmission.  rtificate of Mailing or Transhis Fee(s) Transmittal is bein	ent or formal drawing, m			
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3 FC:8001 15.0 APPLICATION NO.	)() DA FILING DATE	FIRST NAMED I	NVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.			
10/625,615	07/24/2003	Hidenori Hasegawa		OKI.217D	7909			
TITLE OF INVENTION: SUBSTRATE FOR MOUNTING A SEMICONDUCTOR CHIP AND METHOD FOR MANUFACTURING A SEMICONDUCTOR DEVICE								
APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE			
nonprovisional	NO	\$ <del>1330</del> . * 1370	\$300	\$ <del>1630</del> *(670	10/26/2004			
EXAM	IINER	ART UNIT	CLASS-SUBCLASS					
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1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).  Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.  "Fee Address" indication (or "Fee Address" Indication form PTO/SB/47; Rev 03-02 or more recent) attached. Use of a Customer Number is required.			<ol> <li>For printing on the patent front page, list</li> <li>the names of up to 3 registered patent attorneys or agents OR, alternatively,</li> <li>the name of a single firm (having as a member a registered attorney or agent) and the names of up to 2 registered patent attorneys or agents. If no name is listed, no name will be printed.</li> </ol>			2	
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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Oki Electric Industry Co., Ltd.

Tokyo, Japan

Please check the appropriate assignee category or categories (will no	ot be printed on the patent);	u individual	corporation or other private group entity	☐ governm
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☑ Issue Fee	A check in the amo	ount of the fee(s)	is enclosed.	
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☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.

□ b. Applicant is not claiming SMALL ENTITY status. See, e.g., 37 CFR 1.27(g)(2).

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TRANSMIT	TAL OF PAYME	Docl	Docket No.					
	(37 <b>g</b> .)	OF	KI.217D					
Applicant(s): Hidenori Hasegawa								
Application No. Filing Date EXTERNE Examiner Customer No. Group Art Unit Confirmation No. 10/625,615 July 24, 2003 V. Mandala 2826 7909								
Invention: SUBS	STRATE FOR MOUN		DUCTOR (	CHIP AND ME	THOD FOR MA	NUFACTURING		
Transmitted herewi	Mail Stop Issue Fee COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, VA 22313-1450  Transmitted herewith are the following for the above-identified application.							
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